

Shri Guru Gobind Singhji Institute of Engineering & Technology, Nanded

Department of Electronics and Telecommunication Engineering



The department of Electronics and Telecommunication Engineering was established in the year 1981 with a sanctioned intake of 30 students. In view of future demand in this field the intake was increased to 60 in 1986, 90 in the years 2000 and 120 in the year 2001. The post graduate program in Electronics Engineering was started with a sanctioned intake of 18 in 1987. The department was accredited by NBA in February 2003 and June 2008. The faculty is well qualified, dynamic and experienced. The department is consistently achieving good academic results. The placement on the campus of the department is progressively increasing in autonomy. The department has strong research groups in digital signal processing, speech, computer vision, pattern recognition, VLSI Design and Embedded Systems.

Faculty Details

SN	Name	Designation	Qualification	Specialization	Experience (in years)	Publications	
						Journal	Conference
1	Dr. Y. V. Joshi	Professor	Ph. D.	Digital signal processing, VLSI design	22	6	20
2	Dr. S. N. Talbar	Professor & Head	Ph. D.	Digital Image processing, Embedded systems	23	11	50
3	Dr. D. D. Doye	Professor	Ph. D.	Speech processing, Neural networks	20	2	11
4	A. N. Kamthane	Assistant Professor	M. E.	Computer networks, Microcontrollers	28	--	10
5	Dr. R. R. Manthalkar	Assistant Professor	Ph. D.	Texture analysis and VLSI design	20	4	10
6	Dr. S. V. Bonde	Assistant Professor	Ph. D.	Biomedical signal processing	20	--	1
7	Dr. S. S. Gajre	Lecturer (Sel. grade)	Ph. D.	Biomedical signal processing, EDC, and VLSI design	18	3	6
8	Mrs. U. R. Kamble	Lecturer (Sel. grade)	Ph. D.	Digital Electronics, Digital Image Processing	18	--	1
10	Dr. M. B. Kokare	Lecturer (Sel. grade)	Ph. D.	Computer vision, Content based image retrieval, pattern recognition	15	6	12

11	A. B. Gonde	Lecturer (Sel. grade)	M. E.	Image processing, Data compression, Electromagnetic Engineering	18	--	1
12	Dr. A. V. Nandedkar	Sr. Lecturer	Ph. D.	Pattern recognition, Neural networks	13	3	10
13	V. P. Ghanwat	Lecturer (Sel. grade)	M. E.	Communications, Electronics circuit design	18	--	--
14	A. I. Tamboli	Lecturer	M. Tech.	Computer networks, Electronics devices and circuits	15	--	2
15	Dr. J. M. Karlekar	Lecturer	Ph. D.	Digital communications, Video and Image processing	18	1	13

In addition to above faculty strength, 12 faculty members are working on contract basis.

Many faculty members shouldered administrative responsibilities as listed below:

- Dr. Y.V. Joshi worked as Head of the department (HoD)(2002-2004), Dean-Academic (June 2004-December 2006), Dean- Resource Mobilization (2007-08)
- Dr. S. N. Talbar worked as HoD (2001), Registrar (August 2004– April 2007), and *is presently working as a Head of the department since June 2008*
- Dr. D. D. Doye worked as HoD (May 2004- December 2006), and is working as Registrar since April 2007
- Prof. A.N. Kamthane worked as HoD (January 2005 – May 2007)
- Dr. M. B. Kokare is working as Controller of Examinations since June 2008


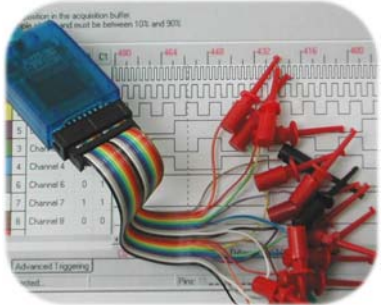






Dr. S.N. Talbar

Laboratories

The department has well equipped laboratories, which are consistently upgraded through various funding from AICTE and DST. The list of major equipments in the laboratories is presented below:



SN	Name of the lab	Available floor area (sq. m.)	Major Equipments
1	Electronics Devices Circuit	94	200 MHz digital storage oscilloscope, spectrum analyzer, digital multi-meters, function generators, power supplies, Oscilloscopes, True RMS Voltmeter, Distortion Factor Meter, Logic Analyzer 
2	Power electronics	94	Oscilloscopes, Digital storage oscilloscope(200mHz), digital multi-meters, function generators, power supplies, Single and 3 phase fully/half controlled bridge, DC motor control, Chopper, Power Electronics Simulation Software (PLECS), etc. 
3	Communication Engineering	94	Arbitrary waveform and function generator (Rhodes and Schwartz - 3 GHz), 200 MHz digital storage oscilloscope, spectrum analyzer, CDMA trainer, Mobile phone trainer, Satellite communication trainer, color TV trainer, AM transmitter, super-heterodyne receiver, Digital modulation/demodulation trainer, ISDN Trainer, Fiber Optics Trainer, Plasma TV, Microwave Bench 
4	VLSI design	94	VLSI universal trainer board Spartan III, Virtex II boards, Mircowind (5 licenses), , Matlab, Xilinx ISE, Tanner tools, Mentor Graphics FPGA advantage, Active HDL, Synopsis, simplicity tools, ICAP/4 (intosoft), Sun blade 2500 work station, HP P4 desktops (20), FPGA Kits (20), Tanner Tools Complete VLSI design System 
5	Computer/Image processing Lab.	94	Digital camera, video camera, imaging table, frame grabber card color/gray with CCD, Document camera, Matlab, Imaging software with GUI and SDK interface, sound recording system, HP P4 desktops (30), Microsoft XP, Microsoft Visual Studio 
6	Embedded system Lab	94	ARM 7TDMI kits (10), 8051 kits (10), MSP 430 kit, 8051 simulator with C cross compiler, DSP kits 6711, Code Composer Studio, Proteus VSM (microcontroller system simulator), HCL P4 desktops (11), ARM 9 Kit, Scanner etc. 
7	Language Lab	94	Acer P-IV Computers (30), Various Communication software



All the computers in the department are connected with Internet with 2 MBPS leased line. The Laboratories are made available for the students round the clock for their projects and extra work with help of PG students.

Departmental Library

In addition to the central library, the department has maintained small library for instant access to students with help of students. The most of books, journals and project reports are donated by faculty members from the department as well as from IIT. The department students help us in maintaining the record of books, journals and project reports. The following table gives details of library:



SN	Description	No. Available
1	Books	1000
2	Journals	IEL online and Springer online
3	CDs, VCD, Multimedia	500

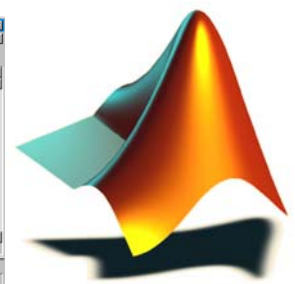
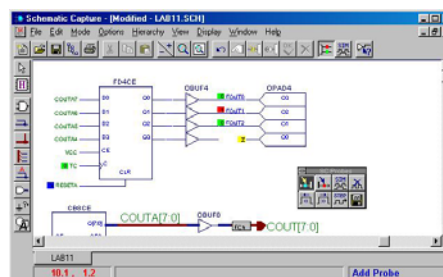
We have maintained the VCDs created by IIT Professors for many courses. The students are using these CD's, and VCD's as an additional learning source for the specifics topics of interest.

Lecture Halls

All the lecture halls of the department are equipped with modern teaching aids such as Computers with internet connection, LCD projectors (OHP), PA system. Using these teachers present latest happening in the field. The institute has developed Media centre under TEQIP scheme. It facilities for Video conferencing, EDUSAT receiving terminal (AICTE Scheme), all other audio visual aids. The facility is looked after by Prof. Y.V. Joshi and Prof. V. P. Ghanwat. The EDUSAT facility is used to watch the countrywide classroom.

Software available

Microsoft XP, Microsoft Visual studio, MS office 2007, Microwind, Matlab, Xilinx ISE, Tanner tools, FPGA advantage, Active HDL, Synopsis, simplicity tools, ICAP/4 (intsoft), Multisim, ComSim, Microcap, 8051 simulator with C cross compiler, Code Composer Studio, Proteus VSM (microcontroller system simulator)



Sponsored Research



The department is encouraging the faculty members to avail the sponsored projects funded by DST and AICTE. Recently we received DST-FIST grant to the tune of 47.5 lakhs for improvement of PG laboratory facilities. Following AICTE Research Promotion Schemes (RPS) and MODROB projects are either completed or on going in the department since 2004:

SN	Name of the project	Grant in Lakhs
1	Advanced communication lab.	5.50
2	Centre of excellence in VLSI design	6.00
3	Texture analysis in biomedical image processing	7.60
4	Development of full duplex intelligent communication system for blind and deaf persons	8.82
5	Content Based Medical Image Retrieval , AICTE career award for young teacher received by Dr. M. B. Kokare	10.50
6	DST-FIST Post graduate teaching VLSI area	47.50

The department has submitted MODROB/RPS projects worth of Rs.75 lakhs for this academic year.

Continuing Education Programs

The department conducts regularly faculty and staff development programs under the AICTE/ISTE/TEQIP schemes for the benefit of faculty members of various engineering colleges. Experts from IIT/



NIT/R&D Labs/industry are regularly contributing to these courses. The aim of conducting these course is to update the knowledge of all the concerned in the advanced technological area. This is helping us in developing good interaction with industry and research and development laboratories of national repute. List of courses organized/arranged by Faculty members since 2004 are as follows:

1. Dr. Y.V. Joshi, AICTE_STTP "VLSI Design using VHDL", May 24th May 2004 to 5th June 2004. (40 participants)
2. Dr. D. D. Doye, Dr. M.B. Kokare, "Recent Trends in Signal and Image Processing" under TEQIP (Networking Cell), 16-18 Jan. 2006 (55 Participants)
3. Dr. D. D. Doye, A.N. Kamthane "Quality Assurance in Technical Education" under TEQIP, 4-6 May 2006 (40 Participants)
4. Dr. D. D. Doye, Dr. M.B. Kokare, AICTE sponsored two weeks Induction programme on "New Concepts, Methods and Advanced Techniques in Speech Processing, Image Processing and Pattern Recognition" under the Scheme of Staff Development Programme, 15 - 26 May 2006 (25 participants)
5. Dr. M. B. Kokare, Two Day's Workshop on "Intellectual Property Right" under Technical Education Quality Improvement Program at Shri Guru Gobind Singhji Institute of Engineering and Technology, Vishnupuri, Nanded, during 13-14th Aug 2006.

6. Dr. S. N. Talbar, coordinated (Four days) Intensive STTP on “Fundamentals of FPGA and DSP Design Flow”, for M.Tech(Extc) students and Faculty members in association with experts from CGCorEl Programming Solutions Pvt.LTd. Bangalore, (Sandeepani School of VLSI Design) at SGGSIET Nanded, 18 -21 February 2008
7. Dr. S. N. Talbar coordinated Three days workshop on “ARM Processor and Embedded Programming” for M.Tech Students and Faculty members in association with expert from SPJ Systems, Pune (February 2007)
8. Dr. S. N. Talbar coordinated Three days workshop on “TI MSP430 Processor, Interfacing and Programming” for M.Tech Students and Faculty members in association with expert from Gill Instruments, Bangalore (December 2006).
9. Dr. S. N. Talbar coordinated One day workshop on “DSP Tools and Programming” with expert Mr. Sachin Ghayal, from Tata Elexi, Pune for M.Tech and Faculty members (September 2006).
10. Prof. A. N. Kamthane coordinated Three days workshop on “Mind Mentality Management for supporting staff” in association with experts from SGGMCOE, Shegaon, (Budhana) (15th -17th Nov.2007).
11. Prof. A. N. Kamthane coordinated Two weeks “Induction Training Program on Teaching Learning Strategies” at SGGSIET, Nanded in association of experts from NTTIR, Bhopal (25/3/07-27/3/2007) (30 Faculty).
12. Prof. A. N. Kamthane and Dr. S. N. Talbar coordinated FDP “Teaching Strategies” at SGGSIET, Nanded in association of experts from NTTIR, Bhopal (21st -23rd July 2007) (25 Faculty).
13. Prof. A. N. Kamthane coordinated Two days workshop on “8051 Processor, Interfacing and Programming” in association with Ambekar Associates, Aurangabad at S.G.G.S.IE &T, Nanded (6th to 7th May, 2007).
14. Prof. A. N. Kamthane coordinated Two weeks “Staff Development Program” at SGGSIET, Nanded in association of experts from NTTIR, Bhopal (19/11//07-1/12/2207) (25 Faculty).
15. Dr. S. N. Talbar, One week Workshop on “8051 Microcontroller and Embedded Programming”, for students and Faculty members, (50 students and 6 faculty) August 2008.

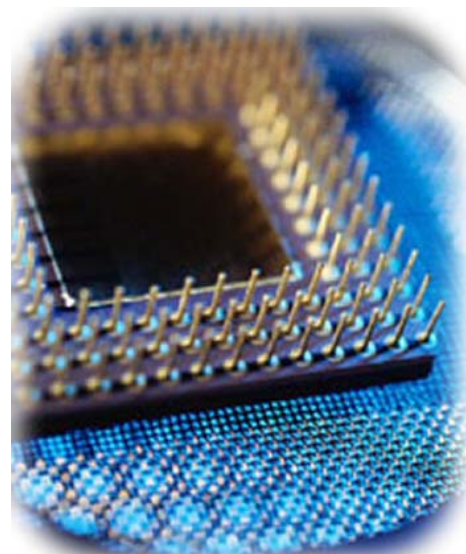
In addition to above list the faculty members of this department have extensively lectured in the short term courses for faculty development conducted at various institutes in the state as well as nearby states. The following is list of Faculty Development programmes conducted by Faculty members of this department at other institutes:

<i>SN</i>	<i>College/Institute</i>	<i>Programme</i>	<i>Faculty Involved</i>
1	BVB College of Engg., Bagalkot, Karnataka	Two days FDP on “ <i>Advances in Signal and Image Processing</i> ”	Dr. M.B. Kokare, Dr. S.N, Talbar, Dr. S.T. Hamade
2.	Govt. College of Engineering, Aurangabad	Three days Workshop on “ <i>Signal/Image Processing</i> ”	Dr. Y.V. Joshi, Dr. R.R. Manthalkar, Dr. M.B. Kokare
3.	Rajarambapu Institute of Technology, Sakharale, Sangli	Two days FDP on “ <i>Digital Signal/Image Processing</i> ”	Dr. S. N. Talbar, Dr. Y.V. Joshi, Dr. R.R. Manthalkar, Dr. M.B. Kokare
4.	Jawaharlal Darda Institute of Technology, Yawatmal	One week STTP on “ <i>Image Processing</i> ”	Dr. S.N. Talbar, Dr. R.R. Manthalkar, Dr. M. B. Kokare

5.	PVP College of Engineering, Mumbai	Two days Workshop on “ <i>Image Processing with MATLAB</i> ”	Dr. S. N. Talbar
6.	Jawaharlal Darda Institute of Engineering and Technology, Yawatmal	STTP on “ <i>MATLAB® and Its Applications</i> ”	Dr. S. S. Gajre

Research and Publications

The department in the late '80s identified the need to inculcate research attitude amongst the Faculty to stay afloat in this competitive world. Strong emphasis was given on this part and as a result, research centre was established in 1994. At the same time faculty members were deputed for Ph.D. at various IITs. At present, majority faculty in the department is having Ph. D. degree. All these teachers have experience of conducting high quality research. This gets reflected in the publications. After the Institute attained the autonomous status, the faculty members are imbibing research attitude among UG/PG students by involving them in research projects.



The department is a recognized research centre by Swami Ramanand Teerth Marathwada University, Nanded. We have 9 faculty members with Ph.D. qualification and 7 are recognized Ph.D. guides by SRTMU, Nanded. The department has been in the fore-front for research activities in Digital Signal Processing, Image Processing, Speech Processing, Neural Network, Biomedical Signal/Image Processing, Advanced Computer networking, VLSI Design and Embedded System etc. It has produced 15 doctorates so far and presently about 40 candidates are registered for Ph.D. programme including one candidate from University of Sana'a, Yemen as a full time research scholar. The faculty members have published more than 40 papers in National and International Journals (including IEE/IEEE/PRL) and about 140 papers in National and International conferences.



<i>Research</i>	Ph.D. awarded	:	15
	Ph.D. registered	:	40
<i>Publications</i>	National/International Journals	:	40
	National/International Conferences	:	140
	Books	:	9

The faculty members are actively engaged in research related activities like working as a reviewer for various National/International Conferences and Journals. Following faculty members are working as reviewers for International Journals:

<i>SN</i>	<i>Name of Faculty</i>	<i>Journal</i>
1.	Dr. M. B. Kokare	IEEE Transactions on System, Man, and Cybernetics Part B IEEE Transactions on Image Processing International Journal of Image Vision and Computing (Elsevier Publication) Pattern Recognition Letter (Elsevier Publication) International Journal of Signal Processing(Elsevier Publication) International Journal of Pattern Analysis and Applications (PAA) SPIE Journal on Electronic Imaging International Journal of Mathematics and Information Signal Processing Letters Conference reviewer for ICVGIP
2.	Dr. R. R. Manthalkar	Pattern Recognition Letters Conference reviewer for ICVGIP
3.	Dr. A.V. Nandedkar	IEEE Transaction on Neural Networks, IEEE Transaction on System, Man and Cybernetics, Pattern Recognition Letters International Journal on Communication Systems Conference reviewer for ICVGIP
4.	Dr. S. N. Talbar	“International Journal of Computing Science & Communication Technologies” APIIT “Journal of Engineering students” published by CAFET-INNOVA Publications, Hyderabad (January 2008)

Dr. Y.V. Joshi and Dr. S.S. Gajre have completed Industrial Training on “Custom VLSI Design Flow” at Sankalp Semiconductors, Pvt. Ltd., Hubli during June 5-22, 2007.

The faculty members are actively involved in book writing activities. The books written by our faculty members are listed below:

<i>SN</i>	<i>Name of Faculty</i>	<i>Book Details</i>
1	Prof. A. N. Kamthane	1. “Programming with ANSI and Turbo C”, Pearson Education, New Delhi, 2002 2. “Object Oriented programming with ANSI and Turbo C++”, Pearson Education, New Delhi, 2003 3. “C and Data structures for JNTU”, Pearson Education, New Delhi, 2004 4. “Introduction to Data structures in C”, Pearson Education, New Delhi, 2005 5. “Programming and Data Structures” for Anna University, Pearson Education, New Delhi, 2006 6. “Computer Programming” for Anna University Pearson Education, 2007
2	Dr. S. N. Talbar	1. “Electronics Circuit Design”, Published by Sadhu-Sudha Publications, Nanded, 1996 (reprint 2001). 2. “Electronics Instrumentation”, Dhanpat Rai Publishing Company (P) Ltd, New Delhi, 2001, coauthored with A.S.Upadhyay. 3. “Electronics Measurements and Instrumentation”, Dhanpat Rai Publishing Company (P) Ltd, New Delhi, Reprint 2008, coauthored with A.S.Upadhyay.

Awards

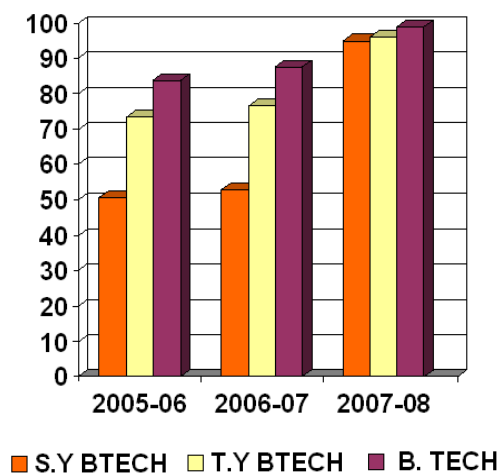
The faculty members have achieved number of award and prizes for their work in technical Education as well as social commitments, which is listed below:



SN	Name of Faculty	Awards
1.	Dr. S. N. Talbar	Young Scientist Award from URSI, Italy, 2004
2.	Kamthane A. N.	Samaj Seva Bhushan award by Shubharam Trust , Banglore Award for outstanding work in IT sector by DVITT Mumbai.
3.	Dr. R. R. Manthalkar	Biography is published in "Who's Who in the World" in 2007 edition.
4.	Dr. S. V. Bonde	Best paper Award at IEEE Conference 'Biovision 2001' in Banglore.
5.	Dr. S. S. Gajre	<ul style="list-style-type: none"> • First Prize in the All India Student Academic Meet in Computer Science in Artificial Intelligence Section for a technical paper. • Second Prize in the National Level Symposium on Computer Science for Students (held by IEEE Students' Branch).
6.	Dr. M.B. Kokare	<ul style="list-style-type: none"> • Received "AICTE Career Award for Young Teachers" for the year 2005-06, from AICTE. • "IAPR Travel Bursary award" for Research paper presented in International conference on Pattern Recognition 2004, Cambridge U. K. • "100% financial support from AICTE" to present research paper in International conference on Pattern Recognition 2004, Cambridge U. K.
7.	Dr. A.V. Nandedkar	<ul style="list-style-type: none"> • Best Teacher award by SGGSIET, Nanded (2003) • "IAPR Travel Bursary award" for Research paper presented in International conference on Pattern Recognition 2006, Hongkong • Inclusion of Biographical profile in who's who in the world 2007 Edition

Academic Results

The students of this department were consistently achieving good results in University system. The trend continued in Autonomous status. The average result of our department is about 80% in autonomous system. In the year 2005-06 Rohit Singewar of our department was University gold medalist. This feat was repeated in 2006-07 by Anandi Iyer. In the first batch of B. Tech. from our institute the Institute gold medalist is our student Sushil Kathade. Academic audit of the department is conducted regularly by the IIT/NIT faculty. The results for last years are shown on the right side.



Our students have bagged many awards at National level competitions. Ms. Soniya Ahuja B.Tech. final year student(2008 batch) was bagged prize for Best Project for Home Security System Tecgfest, IIT Mumbai in year 2007. Ms. Pritee Kumari (2008 Batch) has bagged “Best Non-IITian award” at IIT, Bombay for Mathematics Olympiad in 2007, I at Techfest IIT Bombay, in 2008 and was selected for summer internship by IIT Madras in 2008. Our students are also active in interdisciplinary projects like “BAJA group”, a National level Design competition. Ms. Neha Thakare and Mr. Lalit Awdhwal have participated in academic year 2007-08. Ms. Diya Bajpeyi, Narendra Varma and Apurva Nikade are participating this year’s BAJA event.

We have maintained the VCDs created by IIT Professors for many courses. The students are using these CD’s, and VCD’s as an additional learning source for the specifics topics of interest.

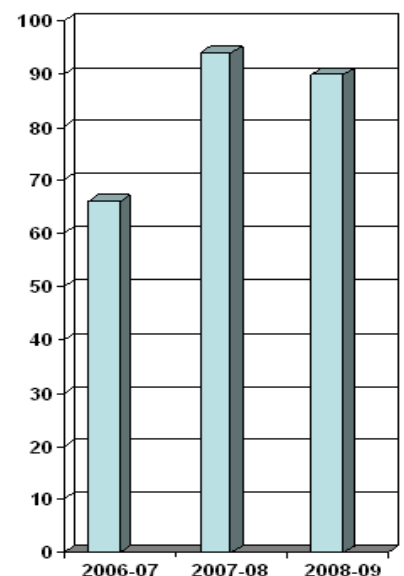
Achievements

As part of the academic work, the final year students have to complete project work under the supervision of the faculty. Selected students are sent for industrial training to the industries to get collaborative work. We have MOUs with Sankalp Semiconductors Pvt. Ltd., Hubli (Karnataka), ni2 logic, Pvt. Ltd., Pune, CEERI Pilani. Our PG students are involved in collaborative project work with CMC Hyderabad, INMAS New Delhi, Motorola India Pvt. Ltd. Bengluru, Robert Bosch Pvt. Ltd. Bengluru, SPJ Systems, Pune.



Students from this department are getting good ranks in GATE examination. In the year 2007 our student Vishal Supekar was ranked 197 in all India ranking and currently pursuing his M. Tech. program in Indian Institute of Technology, Kharagpur. In the year 2006 our student Prakash

Sonkamble was admitted to M. Tech. program in Indian Institute of Science, Bangalore. The successful students in GATE 2007 are (numbers in bracket are the percentile score) Sandip Waikar (97.53), Sushil Kathade (95.5), Manmeet Barad (86), Sachin Navale (87.53), Abhjiti Gune (85.15), Saurabh Chogule (85.15). Many students qualify with good scores for examinations GRE, TOFEL, CAT, and ILTS. Our alumni are well placed in companies of global repute like IBM, Microsoft, Reliance, Infosys, Wipro, Motorola, Texas Instrument, TCS, C-DAC, L&T, Siemens, ST Microelectronic, HP Labs, Tata Elexi, Cognizant, Tech Mahindra, NVIDIA Graphics, Persistent, LG, Sankalp Semiconductors, etc. Mr. Tushar Metkar of 2007 batch has been selected in core company “Marvel”. The placement through campus interviews for last three years is given in the adjacent bar chart.



Students Activity



The students of this department are very active and they do participate in National and international events. Industrial training is a part of a curriculum and every student imparts industrial training at end of third year. PG students are encouraged to do projects in the organization such as CMC ltd Hyderabad, BARC Mumbai, CEERI Pilani, Robert Bosch Bangluru, Sankalp Semiconducters, Hubli, Wavelet Group, Pune etc. 'ECSTASY' a student's initiative of this department organizes aptitude tests, seminars, group discussion, industrial visits, blood donation camp as well as sports and cultural activities, contributing to overall development of the students.

The students publish their articles through Wall magazines "Patheya" and "Technopath". They are conducting inter departmental competition for various sports like Cricket, Chess, Volley Ball etc. The students of this department are also participated in various sport activity at inter College tournament as well as University level. Mr. Batsu Lego represented University as well as a Captain of college team in Football (2007). Miss Dipika Yambal and Miss Smita Patil represented Institute in Table Tennis (women's team). Mr. Depankar Sarkar represented the University as well as a Captain of college team in Table Tennis in year 2007.



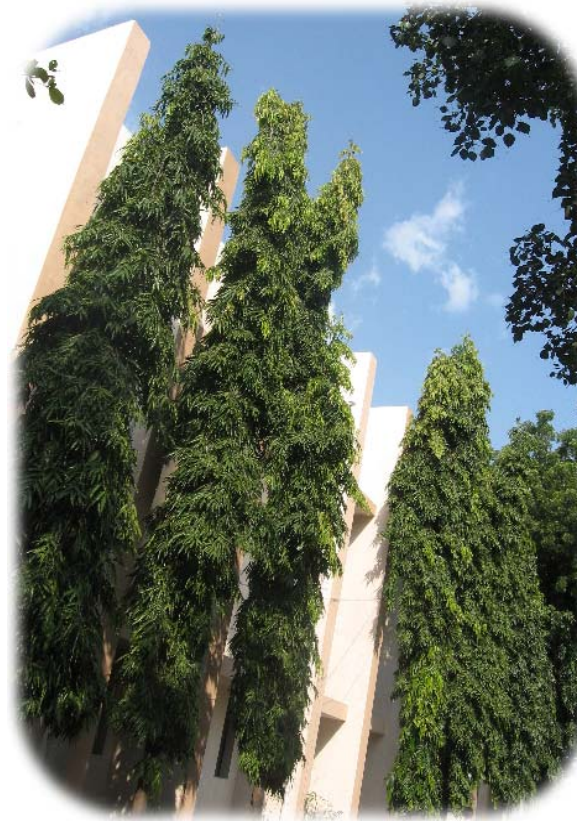
In addition to 'ECSTASY', students have other groups like 'R&D group' and 'ROBOTICS Club' under which they contribute to various technical activities like seminars, workshops, expert lectures, National level Paper presentation competition and project competitions in the respective advanced area of Digital communication, VLSI Design, Microcontroller based applications and Embedded systems. The "Reading circle" group is formed by a enthusiastic group of students in the department. They periodically come together and hold discussions on the books read. Mr. Satyajet Karwa won 1st the prize in National Level Paper presentation at College of Engineering, Pandharpur (Solapur) while Mr. Rahul Mali won the 1st prize in International Paper Presentation held at MIT, Aurangabad. "Pragyaa" is another prestigious activity, initiated by our young and enthusiastic faculty Mr. Vishwanathan Mohan (selected under AICTE EFIP) in the year 2005. This is a National level activity organized by students in which students participate in different activities like Paper presentation, C Programming skills, Projects, Robotics, Contraption, *Krishi-Pragyaa*, Expert Lectures etc. Mr. Shushant Kulkarni B. Tech. (Extc) final year students was coordinator in 2005, while Miss Soniya Ahuja has taken the lead for successive Pragyaa.

Contact Details

Please contact us via any of the following media:

Call:

+91-2462-269201	Head
+91-2462-269202	Dept. Office
+91-2462-269203	Foreman
+91-2462-269212	Dr. Y. V. Joshi
+91-2462-269201	Dr. S. N. Talbar
+91-2462-269213	Dr. D. D. Doye
+91-2462-269241	A. N. Kamthane
+91-2462-269214	Dr. R. R. Manthalkar
+91-2462-269222	Dr. S. V. Bonde
+91-2462-269214	Dr. S. S. Gajre
+91-2462-269217	Mrs. U.R. Kamble
+91-2462-269220	Dr. M. B. Kokare
+91-2462-269218	A. B. Gonde
+91-2462-269218	Dr. A. V. Nandedkar
+91-2462-269220	V. P. Ghanwat
+91-2462-269221	A. I. Tamboli
+91-2462-269216	Dr. J. M. Karlekar



Fax:

+91-2462-229236 (Please write: "Kind Attention: Head, Electronics and Telecom.")

Snail Mail:

Head, Dept of Electronics and Telecommunication Engineering
Shri Guru Gobind Singhji Institute of Engineering and Technology,
Vishnupuri, Nanded (Maharashtra State) PIN 431606 INDIA

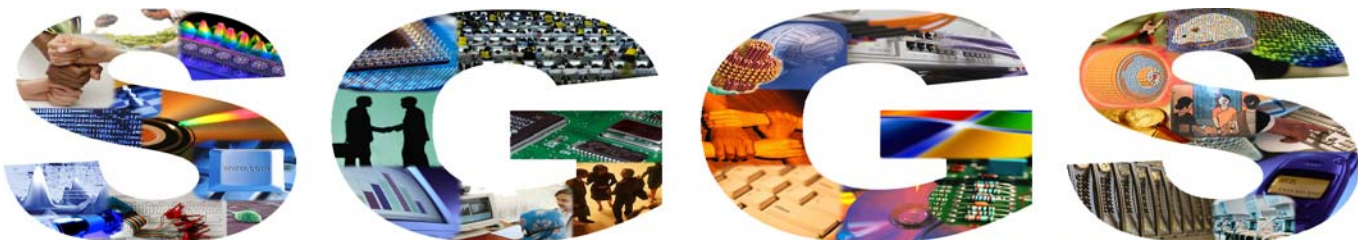
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